

SLM758 W •?ú rB* D

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Ý½, C0& Æ SLM758 W •?ú rB* D

Ý(~ b - Æ V1.02

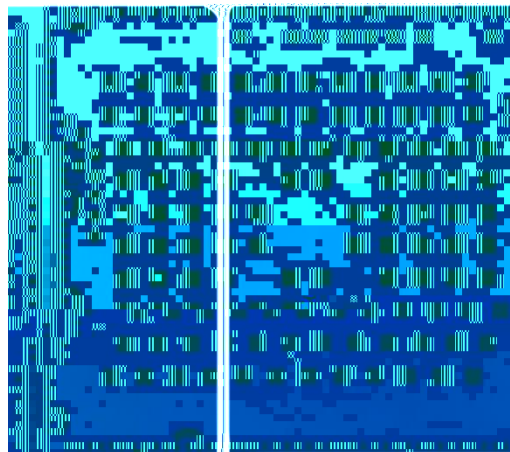
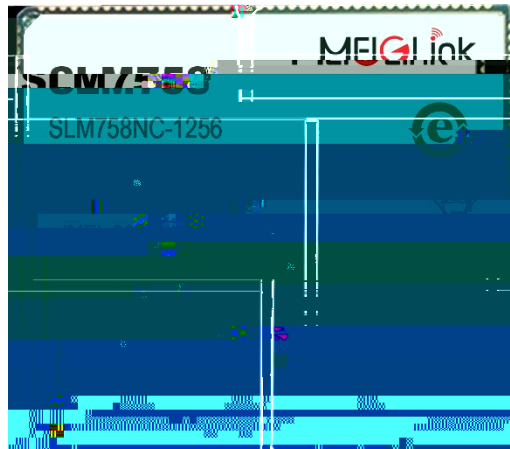
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SLM758

_V1.02



2.



4-lane
13MP
1080p 60fps, VP8/VP9
60 fps: H.264/ +1080p 30fps



		5G WiFi-
		1 MIC
		1 MIC
Audio		1 0.8W D
		1
		1

3.

SLM758-E	TDD-LTE: B38/40/41 FDD-LTE: B1/3/5/7/8/20 WCDMA: B1/B5/B8 GSM: B5/3/8
SLM758-A	FDD-LTE B2/4/5/7/12/13/17/28b WCDMA B2/4/5
SLM758-J	FDD-LTE B1/3/8/9/19/26/28 WCDMA B1/8/9/19

4.SLM758

SLM758 272pin

PIN#	SLM758 Pin name	Pad characteristics	Functional description
1	GND	GND	GND
2	FLASH_LED1+	AO	FLASH LED anode(1A)
3	GND	GND	GND
4	PMI8952_MPP1	AO-Z,AI,DO	Configurable MPP,PWM,ADC
5	LCD_BL_LED_K1	AI	LCD Backlight cathode1(20mA)
6	LCD_BL_LED_K2	AI	LCD Backlight cathode2(20mA)
7	LCD_BL_LED_A	PO	LCD Backlight anode
8	PMI_HAP_OUT_N	AO	Haptics driver output negative
9	PMI_HAP_OUT_P	AO	Haptics driver output positive
10	GND	GND	GND
11	UART2_MSM_TX	B-PD:nppukp	Configurable I/O,UART2 TX
12	UART2_MSM_RX	B-PD:nppukp	Configurable I/O,UART2 RX
13	GPIO6_I2C2_SDA	B-PD:nppukp	Configurable I/O, I2C SDA
14	GPIO7_I2C2_SCL	B-PD:nppukp	Configurable I/O, I2C SCL
15	GPIO12_UART4_TX	B-PD:nppukp	Configurable I/O,UART4 TX
16	GPIO13_UART4_RX	B-PD:nppukp	Configurable I/O,UART4 RX
17	GPIO14_SENSOR_I2C4_SDA	B-PD:nppukp	Configurable I/O,SENSOR I2C SDA
18	GPIO15_SENSOR_I2C4_SCL	B-PD:nppukp	Configurable I/O, SENSOR I2C SCL
19	GPIO42_ACCL_INT1	B-PD:nppukp	Configurable I/O,ACC INT
20	GPIO43_ALSP_INT_N	B-PD:nppukp	Configurable I/O,ALSP INT
21	GPIO44_MAG_INT	B-PD:nppukp	Configurable I/O,MAG INT
22	GPIO45_GYRO_INT	B-PD:nppukp	Configurable I/O,GYRO INT
23	GND	GND	GND
24	GPIO85_KEY1_VOL+	B-PD:nppukp	Configurable I/O,KEY VOL+
25	GPIO86_KEY2_SNAPSHOT	B-PD:nppukp	Configurable I/O,KEY VOL- or SNAPSHOT



			MI2S1_D1
28	GND	GND	GND
29	VREG_L11_2P95	PO	PMIC output 2.95V for SD-card power
30	VREG_L12_PX2_2P95	PO	PMIC output 2.95V for SDC2 signal
31	GPIO133_SDCARD_DET_N	B-PD:nppukp	Configurable I/O,SD card detection
32	SDC2_SDCARD_D0	BH-NP:pdpukp	Secure digital controller 2 data bit 0
33	SDC2_SDCARD_D1	BH-NP:pdpukp	Secure digital controller 2 data bit 1



95	CAM_I2C_SCL1	B-PD:nppukp	Configurable I/O, Dedicated camera I2C1 SCL
96	CAM_I2C_SDA1	B-PD:nppukp	Configurable I/O, Dedicated camera I2C1 SDA
97	GPIO65_TP_INT_N	B-PD:nppukp	Configurable I/O,TP INT
98	GPIO64_TP_RST_N	B-PD:nppukp	Configurable I/O,TP RESET
99	GPIO63	B-PD:nppukp	Configurable I/O,
100	GPIO59	B-PD:nppukp	Configurable I/O,
101	GND	GND	GND
102	DIV_ANT	AI	RF signal for diversity ANT
103	GND	GND	GND
104	CAM_I2C_SCL0	B-PD:nppukp	Configurable I/O, Dedicated camera I2C0 SCL
105	CAM_I2C_SDA0	B-PD:nppukp	Configurable I/O, Dedicated camera I2C0 SDA
106	GPIO54_UIM1_DET	B-PD:nppukp	Configurable I/O,UIM1 removal detection
107	UIM1_RESET	B-PD:nppukp	Configurable I/O,UIM1 reset
108	UIM1_CLK	B-PD:nppukp	Configurable I/O,UIM1 clock
109	UIM1_DATA	B-PD:nppukp	Configurable I/O,UIM1 data
110	VREG_L14_UIM1	PO	



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159	VBUS	PI,PO	USB Voltage
160	GND	GND	GND



193	MIPI_CSI0_LANE1_P	AI	MIPI camera serial interface 0 lane1+
194	MIPI_CSI0_LANE1_N	AI	MIPI camera serial interface 0 lane1-

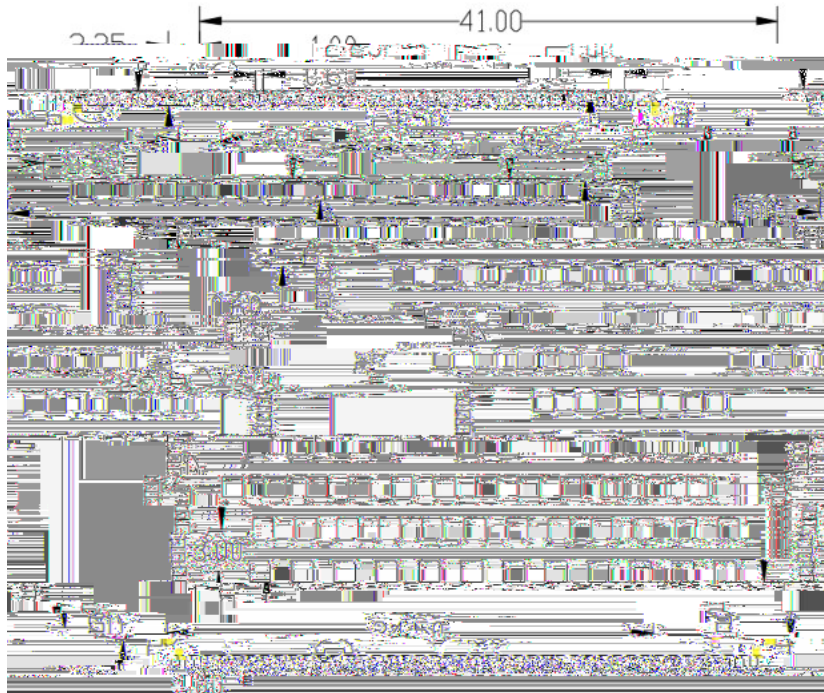


224	GND	GND	GND
225	MIPI_DS11_LANE0_P	AO	MIPI display serial interface 1 lane0+

2D

TOP

BOTTOM:



Side:

